



PICO821 Series
Intel[®] Atom[™] All-In-One
Pico ITX CPU Board
User's Manual



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ESD Precautions

Computer boards have integrated circuits sensitive to static electricity. To prevent chipsets from electrostatic discharge damage, please take care of the following jobs with precautions:

- Do not remove boards or integrated circuits from their anti-static packaging until you are ready to install them.
- Before holding the board or integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. It discharges static electricity from your body.
- Wear a wrist-grounding strap, available from most electronic component stores, when handling boards and components.

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CHAPTER 1 INTRODUCTION



The **PICO821** is a Pico-ITX board with support for Intel® ATOM™ processor Z500 Series, and integrates chipset Intel® System Controller Hub US15W that deliver outstanding system performance through high-bandwidth interfaces, multiple I/O functions for interactive applications and various embedded computing solutions. The board has one 200-pin unbuffered SODIMM sockets for DDR2 400/533 MHz SO-DIMM memory, maximum memory capacity up to 2GB. It also features Gigabit/Fast Ethernet, four USB 2.0 high speed compliant and one USB client port, one serial port (1x RS-232), and built-in high definition audio codec that can achieve the best stability and reliability for industrial applications. It provides you with unique embedded features, such as all internal I/O connections and Pico-ITX form factor that applies an extensive array of PC peripherals. The board can be enhanced by its built-in watchdog timer, a special industrial feature not commonly seen on other motherboards.

1.1 Specifications

CPU

- Intel® ATOM™ processor Z500 Series

● System Chipset

- Intel® System Controller Hub US15W

● BIOS

- Phoenix-Award BIOS, Y2K compliant
- 8Mbit FWH Flash, DMI, Plug and Play
- SmartView for multiple LCD type selection, display mode option and application extension features
- “Load Optimized Default” to backup customized Setting in the BIOS flash chip to prevent from CMOS battery fail

● System Memory

- One x 200-pin unbuffered DDR2 SODIMM socket
- Maximum to 2GB DDR2 400/533 MHz memory

● Onboard Multi I/O

- Controller: Winbond W83627HG
- Serial Ports: one port for RS-232

● CompactFlash™ Socket

- One CompactFlash™ Type II low profile slot

● USB Interface

- Four USB ports with fuse protection and complies with USB Spec. Rev. 2.0
- One USB client port

● Display

- One 2 x 10-pin LVDS connector, one 7-pin wafer connector for inverter control

● Watchdog Timer

- 1~255 seconds; up to 255 levels

- **Ethernet**
 - One port with RTL8111B for Gigabit/Fast Ethernet
 - One 14-pin wafer connector
- **Audio**
 - HD Audio compliant (with Speaker/line-out & MIC-in) via ALC888
 - Supports multi-channel audio stream, 32-bit sample depth, and sample rate up to 192KHz
 - Lin-in/Line-out/MIC-in
- **Power Management**
 - ACPI (Advanced Configuration and Power Interface)
- **Form Factor**
 - Pico-ITX form factor
- **Power Supply**
 - +5V (+/-5%) only
- **Power Consumption**
 - 5V@ 1.75A Idle (Z510 1.1GHz with 1GB DDR2, CF)
 - 5V@ 1.93A Burn-in (Z510 1.1GHz with 1GB DDR2, CF)



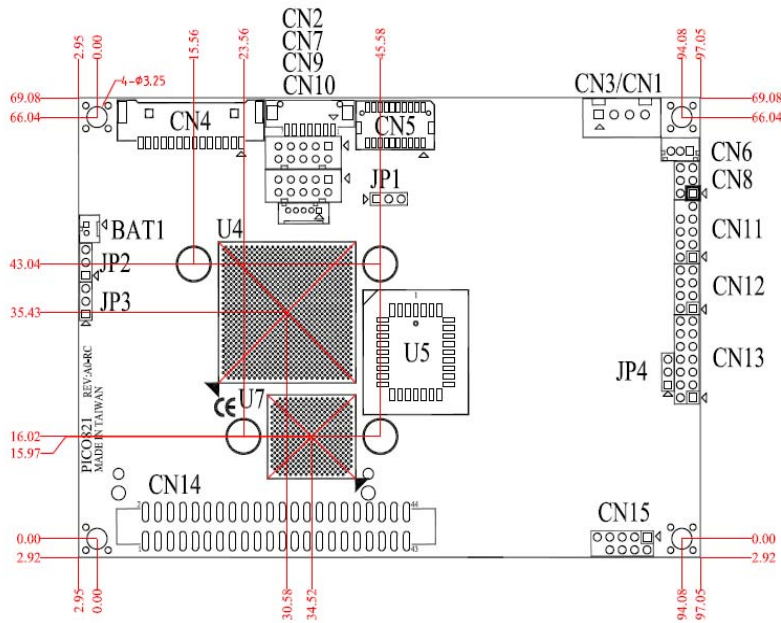
NOTE All specifications and images are subject to change without notice.

1.2 Utilities Supported

- Chipset Driver
- Ethernet Driver
- Graphic Drivers
- Audio Drivers

CHAPTER 2 JUMPERS AND CONNECTORS

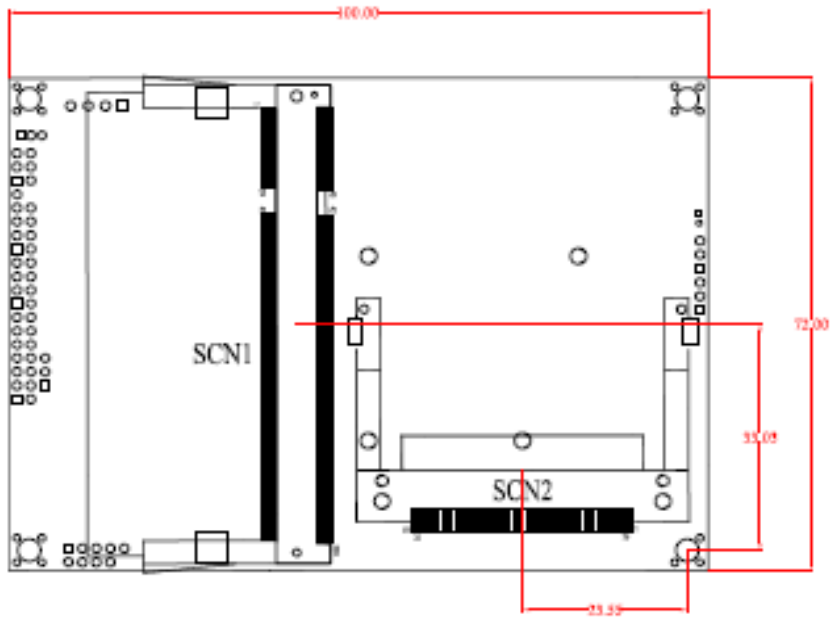
2.1 Board Dimensions and Fixing Holes




Component Side



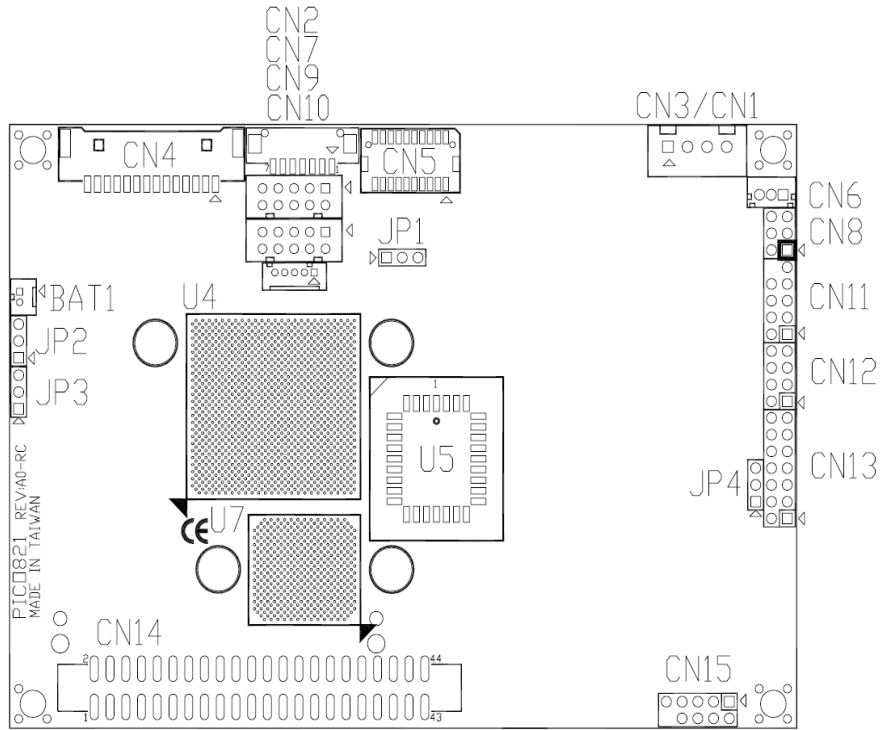
NOTE The height of Component Side is 18mm.



Solder Side

 **NOTE** The height of Solder Side is 9mm.

2.2 Board Layout



Component Side

Side



2.3 Jumper Settings

Proper jumper settings configure the **PICO821** to meet your application purpose. We are herewith listing a summary table of all jumpers and default settings for onboard devices, respectively.

Jumper	Default Setting	Jumper Setting
JP1	Flat Panel Power Selection: Optional or Default : 3.3V	Short 1-2
JP2	Clear CMOS Setting: Normal	Short 1-2
JP3	CompactFlash Voltage Selection Optional or Default : 3.3V	Short 1-2
JP4	AUTO BUTTON Mode Selection Optional or Default : OFF	Short 2-3



2.3.1 Flat Panel Connector Voltage Selection Jumper (JP1)

The board supports +3.3V or +5V flat panel displays. Configure the jumper **JP1** to the appropriate voltage of the flat panel (CN5).

Description	Function	Jumper Setting
Flat Panel Connector (CN5) Voltage Selection	3.3V (Default)	
	5V	



2.3.2 CMOS Clear Jumpers (JP2)

You may need to use this jumper is to clear the CMOS memory if incorrect settings in the Setup Utility.

Description	Function	Jumper Setting
CMOS Clear	Normal (Default)	
	Clear CMOS	



2.3.3 CompactFlash™ Power Selection Jumper (JP3)

This jumper is to select the voltage for CompactFlash™ interface.

Description	Function	Jumper Setting
CompactFlash™ Power Select	3.3V (Default)	
	5V	

2.3.4 AUTO BUTTON Mode Selection Jumper (JP4)

The jumper selects the AUTO BUTTON Mode.

Description	Function	Jumper Setting
AUTO BUTTON Mode Selection	ON	
	OFF (Default)	

2.4 Connectors

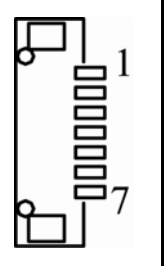
Connectors connect the board with other parts of the system. Loose or improper connection might cause problems. Make sure all connectors are properly and firmly connected. Here is a summary table shows you all connectors on the **PICO821** Series.

Connectors	Label
Inverter Connector	CN2
DC Power Connector	CN3/CN1
LAN Connector	CN4
LVDS Connector	CN5
SMBUS Connector	CN6
USB4&5 Connector	CN7
KB/MS Connector	CN8
USB0&1 Connector	CN9
USB CLIENT Connector	CN10
COM Port Connector	CN11
DIO Port Connector	CN12
Flat Panel Bezel Connector	CN13
PATA Connector	CN14
AUDIO Connector	CN15
DDRII SODIMM Connector	SCN1
Compact Flash Connector	SCN2

2.4.1 LVDS Backlight Connector (CN2)

The **CN2** is DF13-7S-1.25C 7-pin connectors for inverter that we strongly recommend you to use the matching DF13-7S-1.25C connector.

Pin	Signal
1	+5V
2	+5V
3	ENABLE +5V
4	ENABLE +3.3V
5	BKLTCTL
6	GND
7	GND

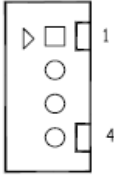


2.4.2 DC POWER Connector (CN3 co-layout CN1)

The **CN3** is a 2.0mm wafer DC Power connector for DC 5V input.

CN1 is a 2.0mm 90 degree wafer DC Power connector for DC 5V input.

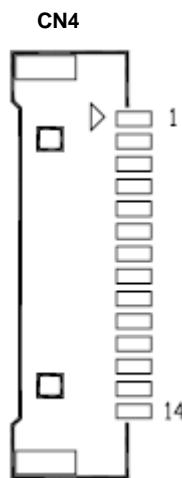
Pin	Signal
1	+5V SBY
2	+5V SBY
3	GND
4	GND



2.4.3 LAN Connector (CN4)

CN4 is an LAN connector that connects the PICO821 to a 10-Base-T, 100-Base-T or Gigabit hub just by plugging one end of the cable to LAN, and connecting the other end (phone jack) to a 10-Base-T, 100-Base-T or Gigabit hub.

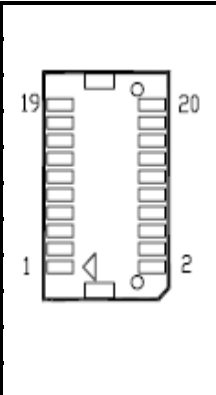
Pin	Signal
1	1000 LAN LED
2	100 LAN LED
3	GND
4	MDI3-
5	MDI3+
6	MDI1-
7	MDI2-
8	MDI2+
9	MDI1+
10	MDI0-
11	MDI0+
12	N.C.
13	Link LED
14	Active LED-



2.4.4 LVDS Flat Panel Connector (CN5)

The LVDS connector on the PICO821 is a 20-pin connector. It is strongly recommended to use the matching JST SHDR-20V-S-B connector.

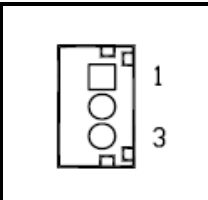
Pin	Signal	Pin	Signal
1	VCCM	2	VCCM
3	VCCM	4	VCCM
5	Channel A D0-	6	Channel A D3-
7	Channel A D0+	8	Channel A D3+
9	GND	10	GND
11	Channel A D1-	12	Channel A CLK-
13	Channel A D1+	14	Channel A CLK+
15	GND	16	GND
17	Channel A D2-	18	GND
19	Channel A D2+	20	GND



2.4.5 SMBUS Connector (CN6)

Connector CN6 is for SMBUS interface support.

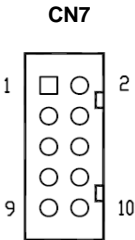
Pin	Signal
1	CLOCK
2	DATA
3	GND



2.4.6 USB Port4/5 Connector (CN7)

These Universal Serial Bus (USB) connectors on this board are for installing versatile USB interface peripherals. These are 10-pin standard USB connectors.

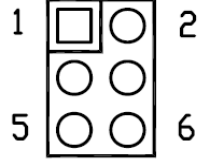
Pin	Signal	Pin	Signal
1	+5V SBY	2	+5V SBY
3	USB4-	4	USB5-
5	USB4+	6	USB5+
7	GND	8	GND
9	GND	10	GND



2.4.7 KB/MS Interface Connector (CN8)

The **CN8** is a 2.0mm Pin header connector for KB/MS Interface Connector.

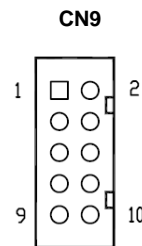
Pin	Signal
1	KBVCC
2	GND
3	KB CLK
4	MS CLK
5	KB DATA
6	MS DATA



2.4.8 USB Port0/1 Connector (CN9)

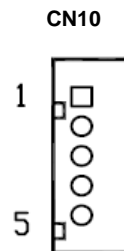
These Universal Serial Bus (USB) connectors on this board are for installing versatile USB interface peripherals. These are 10-pin standard USB connectors.

Pin	Signal	Pin	Signal
1	+5V SBY	2	+5V SBY
3	USB0-	4	SB1-
5	USB0+	6	SB1+
7	GND	8	ND
9	GND	10	ND



2.4.9 USB CLIENT Connector (CN10)

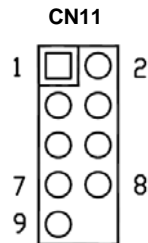
Pin	Signal
1	HOST PC USB POWER DETECT
2	USB2-
3	USB2+
4	GND
5	GND



2.4.10 COM PORT Connector (CN11)

The **CN11** is a 2.0mm Pin header connector for COM Port Interface Connector.

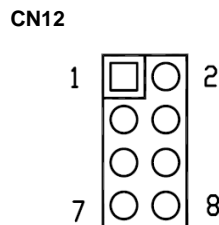
Pin	Signal
1	NDCD1
2	NDSR1
3	NRX1
4	NRTS1
5	NTX1
6	NCTS1
7	NDTR1
8	NRI1
9	GND



2.4.11 DIO Connector (CN12)

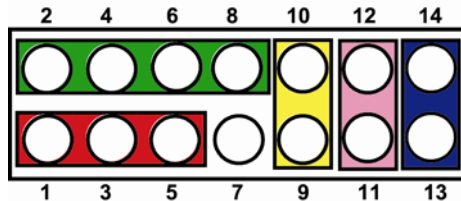
The **CN12** is a 2.0mm Pin header connector for COM Port Interface Connector.

Pin	Signal
1	DIO0
2	DIO3
3	DIO1
4	DIO4
5	DIO2
6	DIO5
7	GND
8	GND



W83627HG-AW GPIO PIN Number	128 (GP10)	127 (GP11)	126 (GP12)	125 (GP13)	124 (GP14)	123 (GP15)
GPIO	Bit 0	Bit 1	Bit 2	Bit 3	Bit 4	Bit 5
CN12	PIN 1	PIN 3	PIN 5	PIN 2	PIN 4	PIN 6

2.4.12 Flat Panel Bezel Connector (CN13)



- **Power LED**

This 3-pin connector denoted as Pin 1 and Pin 5 connects the system power LED indicator to such a switch on the case. Pin 1 is assigned as +, and Pin 5 as -. The Power LED lights up when the system is powered ON.

- **External Speaker and Internal Buzzer Connector**

Pin 2, 4, 6 and 8 can be connected to the case-mounted speaker unit or internal buzzer. While connecting the CPU card to an internal buzzer, please short pins 2-4; while connecting to an external speaker, you need to set pins 2-4 to Open and connect the speaker cable to pin 8 (+) and pin 2 (-).

- **ATX Power On/Off Button**

This 2-pin connector denoted as Pin 9 and 10 connects the front panel's ATX power button to the CPU card, which allows users to control ATX power supply to be power on/off.

- **System Reset Switch**

Pin 11 and 12 can be connected to the case-mounted reset switch that reboots your computer instead of turning OFF the power switch. It is a better way to reboot your system for a longer life of the system's power supply.

- **HDD Activity LED**

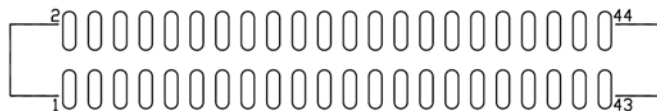
This connection is linked to hard drive activity LED on the control panel. LED flashes when HDD is being accessed. Pin 13 and 14 connect the hard disk drive to the front panel HDD LED, Pin 13 assigned as -, and Pin 14 as +.

2.4.13 IDE Interface Connector (CN14)

The PICO821 has a 44 pin PATA IDE connector have master/slave mode.

Pin	Description	Pin	Description	Pin	Description
1	Reset #	2	GND	3	Data 7
4	Data 8	5	Data 6	6	Data 9
7	Data 5	8	Data 10	9	Data 4
10	Data 11	11	Data 3	12	Data 12
13	Data 2	14	Data 13	15	Data 1
16	Data 14	17	Data 0	18	Data 15
19	GND	20	No Connector	21	DREQ #
22	GND	23	IOW #	24	GND
25	IOR #	26	GND	27	IOCHRDY
28	No Connector	29	Ack #	30	GND-Default
31	Interrupt	32	No Connector	33	SA1
34	PDIAG #	35	SA0	36	SA2
37	HDC CS0 #	38	HDC CS1 #	39	HDD Active #
40	GND	41	VCC	42	VCC
43	GND	44	N.C.		

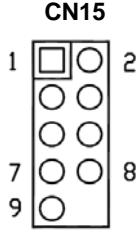
CN14



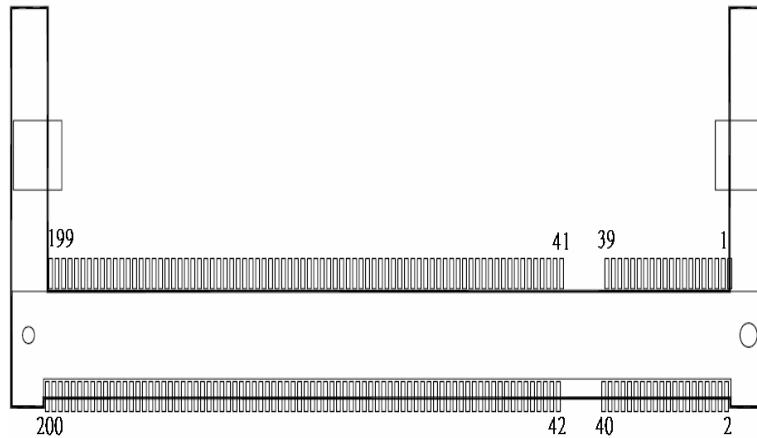
2.4.14 AUDIO Connector (CN15)

The board supports an audio interface. CN15 is a 2.0mm Pin header connector for AUDIO Interface Connector.

Pin	Description	Pin	Description
1	MIC-IN	2	GND
3	Line In L	4	GND
5	Line In R	6	GND
7	Line Out L	8	GND
9	Line Out R		



2.4.15 DDRII SODIMM Connector (SCN1)

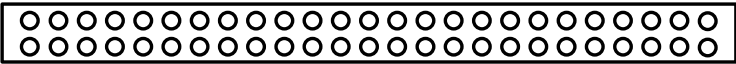


2.4.16 CompactFlash™ Socket (SCN2)

The board is equipped with a CompactFlash™ disk type-II socket on the solder side that supports the IDE interface CompactFlash™ disk card with DMA mode supported. The socket is especially designed to avoid any incorrect installation of the CompactFlash™ disk card.

When installing or removing the CompactFlash™ disk card, please make sure that the system power is off. The CompactFlash™ disk card is defaulted as the C: or D: disk drive in your PC system.

Pin	Signal	Pin	Signal
1	GND	26	CD1-
2	Data 3	27	Data 11
3	Data 4	28	Data 12
4	Data 5	29	Data 13
5	Data 6	30	Data 14
6	Data 7	31	Data 15
7	CS0#	32	CS1#
8	Address 10	33	VS1#
9	ATASEL	34	IORD#
10	Address 9	35	IOWR#
11	Address 8	36	WE#
12	Address 7	37	INTR
13	VCC	38	VCC
14	Address 6	39	CSEL#
15	Address 5	40	VS2#
16	Address 4	41	RESET#
17	Address 3	42	IORDY#
18	Address 2	43	DMAREQ
19	Address 1	44	DMAACK-
20	Address 0	45	DASP#
21	Data 0	46	PDIAG#
22	Data 1	47	Data 8
23	Data 2	48	Data 9
24	IOCS16#	49	Data 10
25	CD2#	50	GND



A diagram of a 50-pin connector, showing two rows of 25 pins each. The pins are represented by small circles arranged in a rectangular grid.

CHAPTER 3

HARDWARE DESCRIPTION

3.1 Microprocessors

The **PICO821** Series supports Intel[®] ATOM[™] processor Z500 Series, which make your system operated under Windows 2000/XP, and Linux environment. The system performance depends on the microprocessor. Make sure all correct settings are arranged for your installed microprocessor to prevent the CPU from damages.

3.2 BIOS

The **PICO821** Series uses Award Plug and Play BIOS with a single 8Mbit FWH Flash, DMI, Plug and Play.

3.3 System Memory

The **PICO821** Series industrial CPU card supports one 200-pin unbuffered DDR2 SODIMM socket for a maximum memory of 2GB DDR2 SDRAMs. The memory module can come in sizes of 64MB, 128MB, 256MB, 512MB, 1GB and 2GB. The Device density supports 512 Mb and 1024-Mb with the width of x16.

3.4 I/O Port Address Map

The Intel ATOM™ processor Z500 Series can communicate via I/O ports. There are total 1KB port addresses available for assignment to other devices via I/O expansion cards.

Address	Devices
000-01F	DMA controller #1
020-02D 024-025 028-029 02C-02D	Interrupt controller #1
02E-02F	Forwarded to LPC(LPC Super I/O 2)
030-031 034-035 038-039 03C-03D	Interrupt controller #2
040-043 050-053	Timer/Counter (8254)
04E-04F	Forwarded to LPC(LPC Super I/O 1)
060-06F	Forwarded to LPC(Microcontroller for Keyboard Controller)
070-077	Real time clock, NMI
080-091	DMA page register
092	Processor I/F(Reset Generator)
093-09F	DMA page register
0A0-0BF	Interrupt controller #2
0C0-0DF	DMA controller #2
0F0	Processor I/F
0F8-0FF	Math processor
170-177	Forward to SATA(SATA Controller)
1F0-1F7	Forward to SATA(SATA Controller)
250-25F	HR I/O
300-31F	Prototype card
376	Forward to SATA(SATA Controller)
378-37F	Parallel Port (LPT)
380-38F	SDLC #2
3A0-3AF	SDLC #1

Address	Devices
3B0-3BF	MDA video card
3C0-3CF	EGA card
3D0-3DF	CGA card
3F6	Forward to SATA (SATA Controller)
3F8-3FF	Serial port #1 (COM1)
3E8-3EF	Serial port #3 (COM3)
2F8-2FF	Serial port #2 (COM2)
2E8-2EF	Serial port #4 (COM4)

3.5 Interrupt Controller

The **PICO821 Series** is a 100% PC compatible control board. It consists of 16 interrupt request lines, and four out of them can be programmable. The mapping list of the 16 interrupt request lines is shown as the following table.

IRQ	Parity check error
IRQ0	System timer output
IRQ1	Keyboard
IRQ2	Interrupt rerouting from IRQ8 through IRQ15
IRQ3	Serial port #2
IRQ4	Serial port #1
IRQ5	PCI Device Share
IRQ7	—
IRQ8	Real time clock
IRQ9	ACPI Controller
IRQ10	—
IRQ11	—
IRQ12	PS/2 Mouse
IRQ13	Math coprocessor
IRQ14	Primary IDE channel
IRQ15	—

CHAPTER 4

PHOENIX-AWARD BIOS UTILITY

The Phoenix-Award BIOS provides users with a built-in Setup program to modify basic system configuration. All configured parameters are stored in a battery-backed-up RAM (CMOS RAM) to save the Setup information whenever the power is turned off.

4.1 Entering Setup

There are two ways to enter the Setup program. You may either turn ON the computer and press immediately, or press the and/or <Ctrl>, <Alt>, and <Esc> keys simultaneously when the following message appears at the bottom of the screen during POST (Power on Self Test).

TO ENTER SETUP PRESS DEL KEY

If the message disappears before you respond and you still want to enter Setup, please restart the system to try it again. Turning the system power OFF and ON, pressing the "RESET" button on the system case or simultaneously pressing <Ctrl>, <Alt>, and keys can restart the system. If you do not press keys at the right time and the system doesn't boot, an error message will pop out to prompt you the following information:

PRESS <F1> TO CONTINUE, <CTRL-ALT-ESC> OR TO ENTER SETUP

4.2 Control Keys

Up arrow	Move cursor to the previous item
Down arrow	Move cursor to the next item
Left arrow	Move cursor to the item on the left hand
Right arrow	Move to the item in the right hand
Esc key	Main Menu -- Quit and delete changes into CMOS Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
PgUp/“+” key	Increase the numeric value or make changes
PgDn/“-“ key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
(Shift) F2 key	Change color from total 16 colors. F2 to select color forward, (Shift) F2 to select color backward
F3 key	Reserved
F4 key	Reserved
F5 key	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu
F6 key	Load the default CMOS value from BIOS default table, only for Option Page Setup Menu
F7 key	Load the Setup default, only for Option Page Setup Menu
F8 key	Reserved
F9 key	Reserved
F10 key	Save all the CMOS changes, only for Main Menu

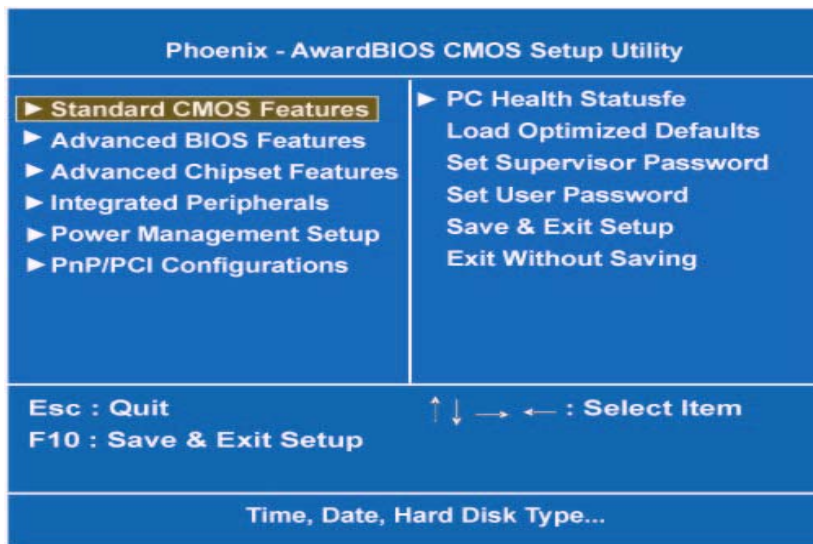
4.3 Getting Help


Main Menu The online description of the highlighted setup function is displayed at the bottom of the screen.

Status Page Setup Menu/Option Page Setup Menu Press <F1> to pop out a small Help window that provides the description of using appropriate keys and possible selections for highlighted items. Press <F1> or <Esc> to exit the Help Window.

4.4 The Main Menu

Once you enter the Award BIOS CMOS Setup Utility, the Main Menu appears on the screen. In the Main Menu, there are several Setup functions and a couple of Exit options for your selection. Use arrow keys to select the Setup Page you intend to configure then press <Enter> to accept or enter its sub-menu.

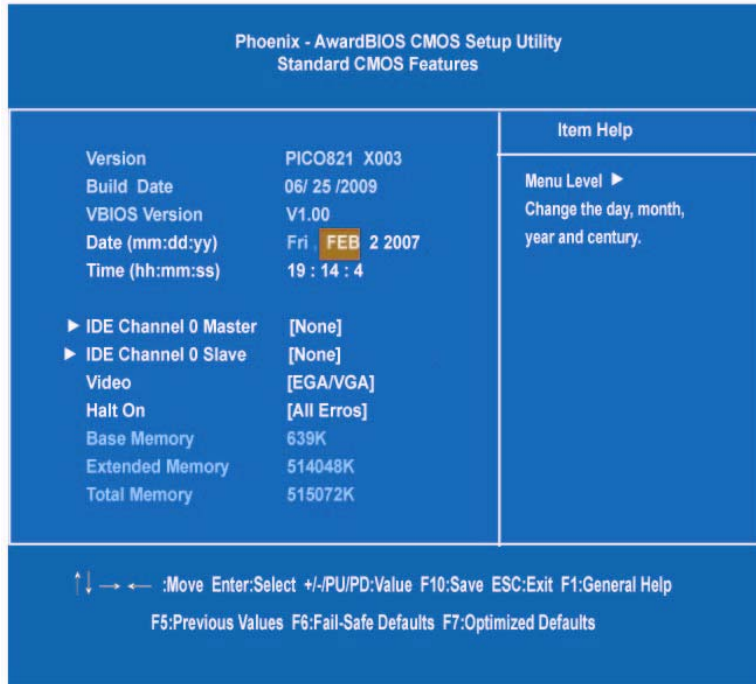


 **NOTE** If your computer can not boot after making and saving system changes with Setup, the Award BIOS will reset your system to the CMOS default settings via its built-in override feature.

It is strongly recommended that you should avoid changing the chipset's defaults. Both Award and your system manufacturer have carefully set up these defaults that provide the best performance and reliability.

4.5 Standard CMOS Setup Menu

The Standard CMOS Setup Menu displays basic information about your system. Use arrow keys to highlight each item, and use <PgUp>



<PgDn> key to select the value you want in each item.

Date The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	It is determined by the BIOS and read only, from Sunday to Saturday.
date	It can be keyed with the numerical/ function key, from 1 to 31.
month	It is from January to December.
year	It shows the current year of BIOS.

Time This item shows current time of your system with the format <hour> <minute> <second>. The time is calculated based on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

IDE Primary Master/Primary Slave

These items identify the types of each IDE channel installed in the computer. There are 45 predefined types (Type 1 to Type 45) and 2 user's definable types (Type User) for Enhanced IDE BIOS. Press <PgUp>/<+> or <PgDn>/<-> to select a numbered hard disk type, or directly type the number and press <Enter>. Please be noted your drive's specifications must match the drive table. The hard disk will not work properly if you enter improper information. If your hard disk drive type does not match or is not listed, you can use Type User to manually define your own drive type. If selecting Type User, you will be asked to enter related information in the following items. Directly key in the information and press <Enter>. This information should be provided in the documentation from your hard disk vendor or the system manufacturer. If the HDD interface controller supports ESDI, select "Type 1". If the HDD interface controller supports SCSI, select "None". If the HDD interface controller supports CD-ROM, select "None".

CYLS.	number of cylinders	LANDZONE	landing zone
HEADS	number of heads	SECTORS	number of sectors
PRECOMP	write precom	MODE	HDD access mode

If there is no hard disk drive installed, select NONE and press <Enter>.

Video

Select the display adapter type for your system.

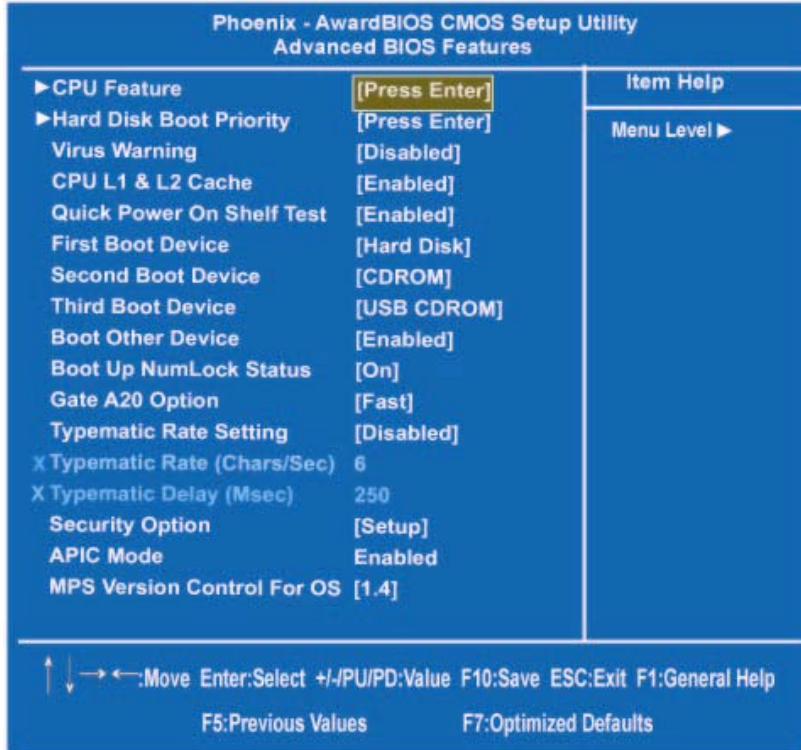
Halt On This item determines whether the system will halt or not, if an error is detected while powering up.

No errors	The system booting will halt on any errors detected. (default)
All errors	Whenever BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system booting will not stop for a keyboard error; it will stop for other errors.

Press <Esc> to return to the Main Menu page.

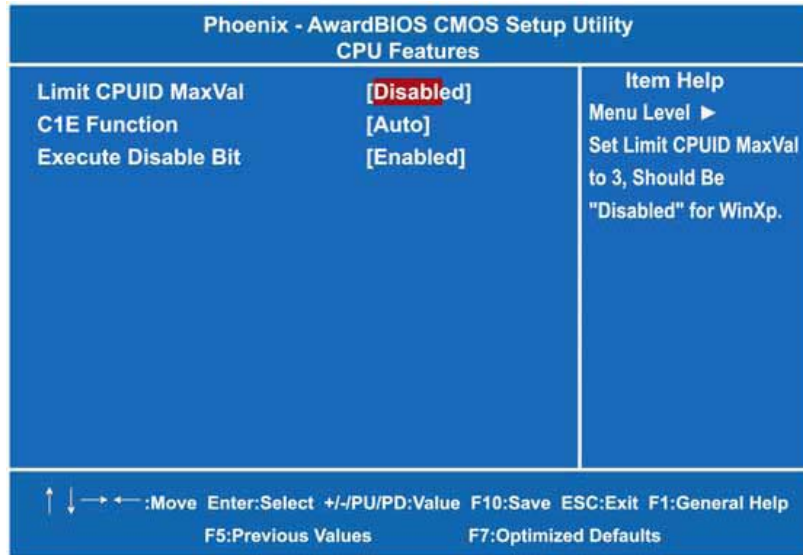
4.6 Advanced BIOS Features

This section allows you to configure and improve your system, to set up some system features according to your preference.



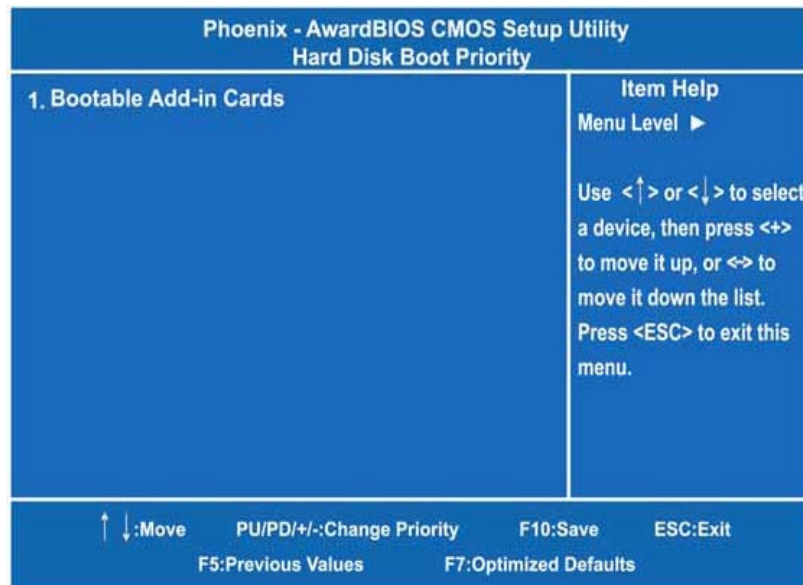
CPU Features

Scroll to this item and press <Enter> to view the CPU Feature sub menu.



Harddisk boot priority

Scroll to this item and press <Enter> to view the sub menu to decide the disk boot priority



- **Quick Power On Self Test**

This option speeds up Power on Self Test (POST) after you turn on the system power. If set as Enabled, BIOS will shorten or skip some check items during POST. The default setting is “Enabled”.

Enabled	Enable Quick POST
Disabled	Normal POST

- **First/Second/Third Boot Device**

These items let you select the 1st, 2nd, and 3rd devices that the system will search for during its boot-up sequence. There is a wide range of options for your selection.

- **Boot Other Device**

This item allows the user to enable/disable the boot device not listed on the First/Second/Third boot devices option above. The default setting is “Enabled”.

- **Boot Up NumLock Status**

Set the the Num Lock status when the system is powered on. The default value is “On”.


- **Security Option**

This item allows you to limit access to the system and Setup, or just to Setup. The default value is “Setup”.

System
Setup

If a wrong password is entered at the prompt, the system will not boot, the access to Setup will be denied, either.

If a wrong password is entered at the prompt, the system will boot, but the access to Setup will be denied.

 **NOTE** To disable the security, select **PASSWORD SETTING** at Main Menu and then you will be asked to enter a password. Do not type anything, just press <Enter> and it will disable the security. Once the security is disabled, the system will boot and you can enter Setup freely.

APIC Mode Use this item to enable or disable APIC (Advanced Programmable Interrupt Controller) mode that provides symmetric multi-processing (SMP) for systems.

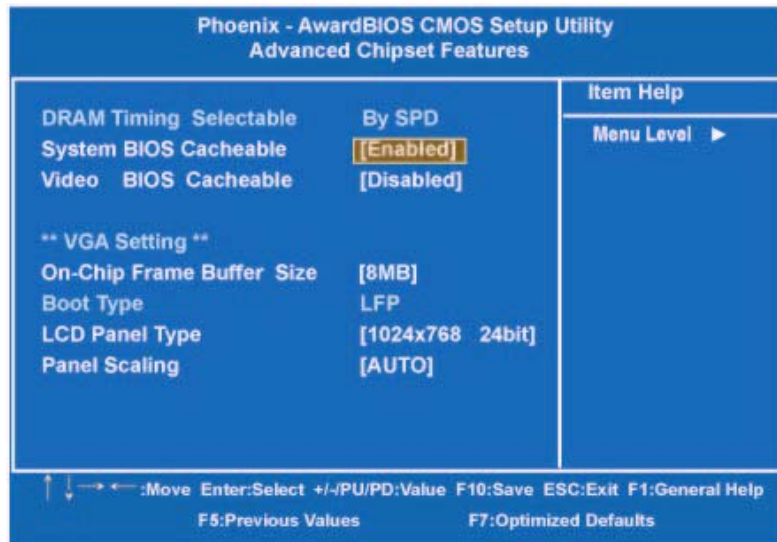
MPS Version Control For OS

This item specifies the version of the Multiprocessor Specification (MPS). Version 1.4 has extended configuration tables to improve support for multiple PCI bus configurations and provide future expandability.

Press <Esc> to return to the Main Menu page.

4.7 Advanced Chipset Features

This section contains completely optimized chipset's features on the board that you are strongly recommended to leave all items on this page at their default values unless you are very familiar with the technical specifications of your system hardware.



DRAM Timing Selectable Use this item to increase the timing of the memory. This is related to the cooling of memory.

System BIOS Cacheable Selecting Enabled allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result. The default value is "Disabled".

Video BIOS Cacheable This item allows you to change the Video BIOS location from ROM to RAM. Video Shadow will increase the video speed.

*** VGA Setting ***

On-Chip Frame Buffer Size

Use this item to set the VGA frame buffer size.

Boot Type

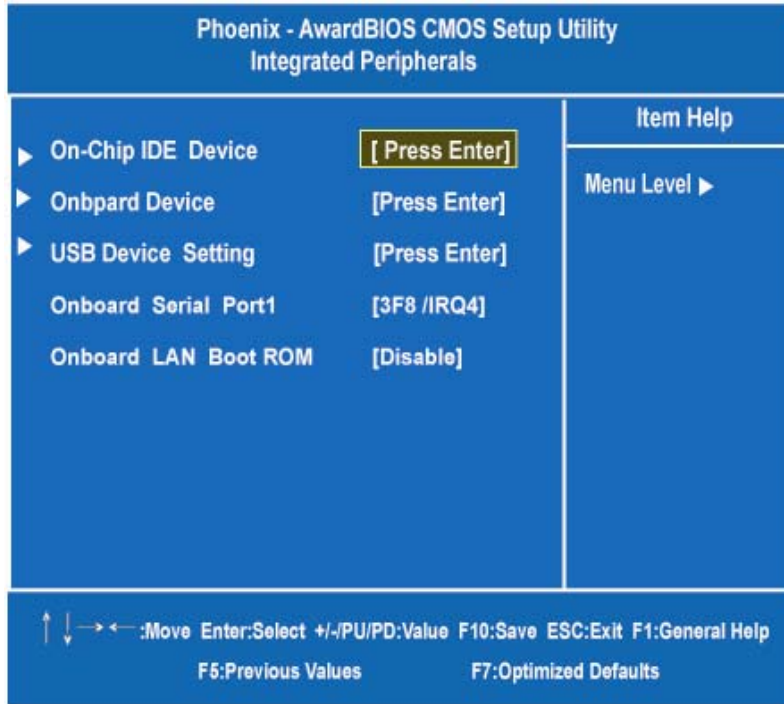
This item is to select Display Device that the screen will be shown.

Panel Scaling This item shows the setting of panel scaling and operates the scaling function that the panel output can fit the screen resolution connected to the output port.

Press <Esc> to return to the Main Menu page.

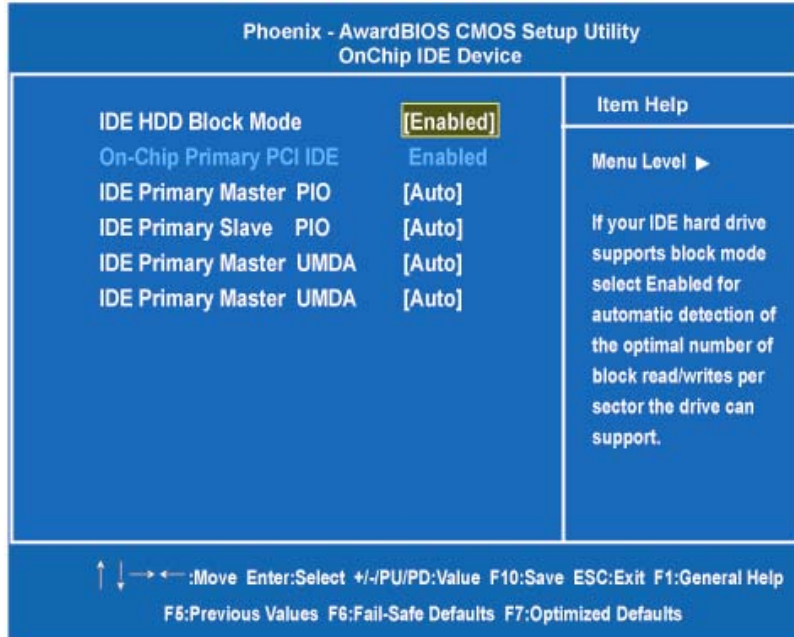
4.8 Integrated Peripherals

This section allows you to configure your OnChip IDE Device, SuperIO Device and Onboard Device.



OnChip IDE Device

Scroll to this item and press <Enter> to view the sub menu OnChip IDE Device.



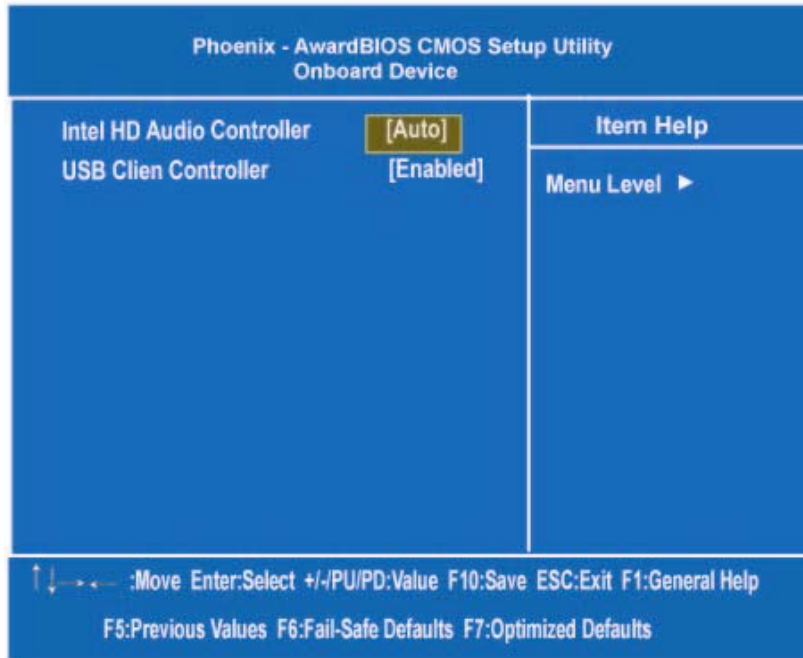
IDE HDD Block Mode

Block mode is also called block transfer, multiple commands, or multiple sectors read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

Press <Esc> to return to the Integrated Peripherals page.

Onboard Device

Scroll to this item and press <Enter> to view the sub menu Onboard Device.

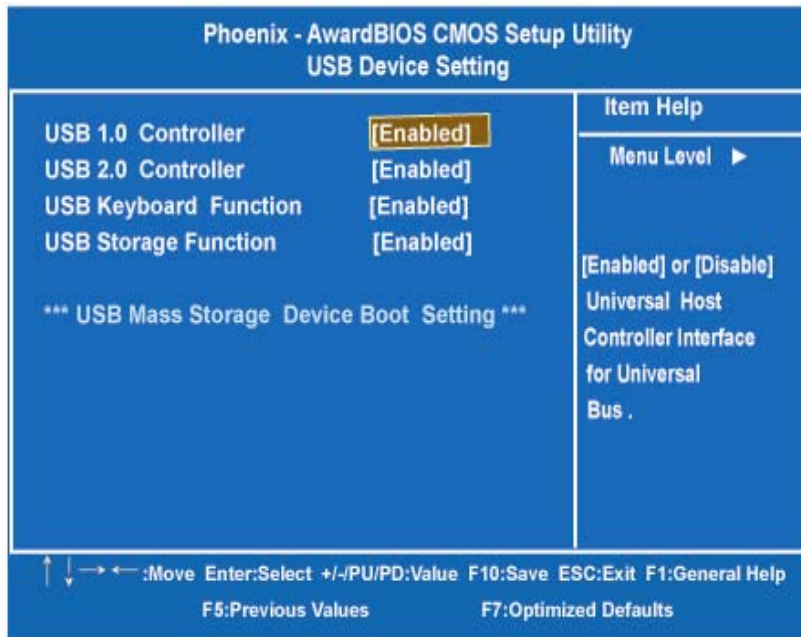


Intel HD Audio Controller

Use this item to enable an Intel HD Audio controller. Press <Esc> to return to the Integrated Peripherals page.

USB Device Setting

Scroll to this item and press <Enter> to view the sub menu USB Device Setting.

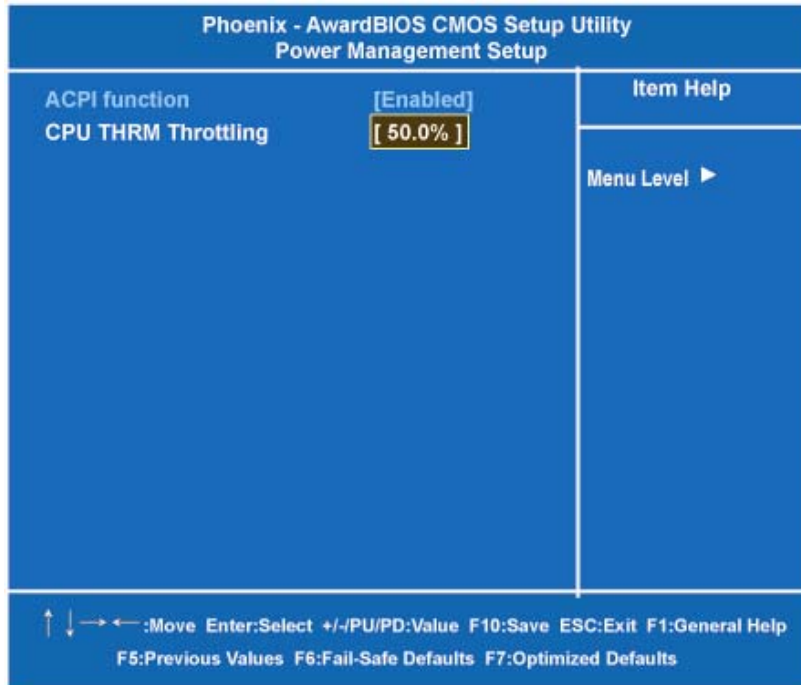


Onboard Serial Port 1/2 Select an address and corresponding interrupt for the serial port. There are several options for your selection.

Onboard Lan Boot ROM Use this item to enable or disable the Boot ROM function of the onboard LAN chip when the system boots up.

4.9 Power Management Setup

The Power Management Setup allows you to save energy of your system effectively. It will shut down the hard disk and turn OFF video display after a period of inactivity.



ACPI Function

Advanced Configuration and Power Management (ACPI).

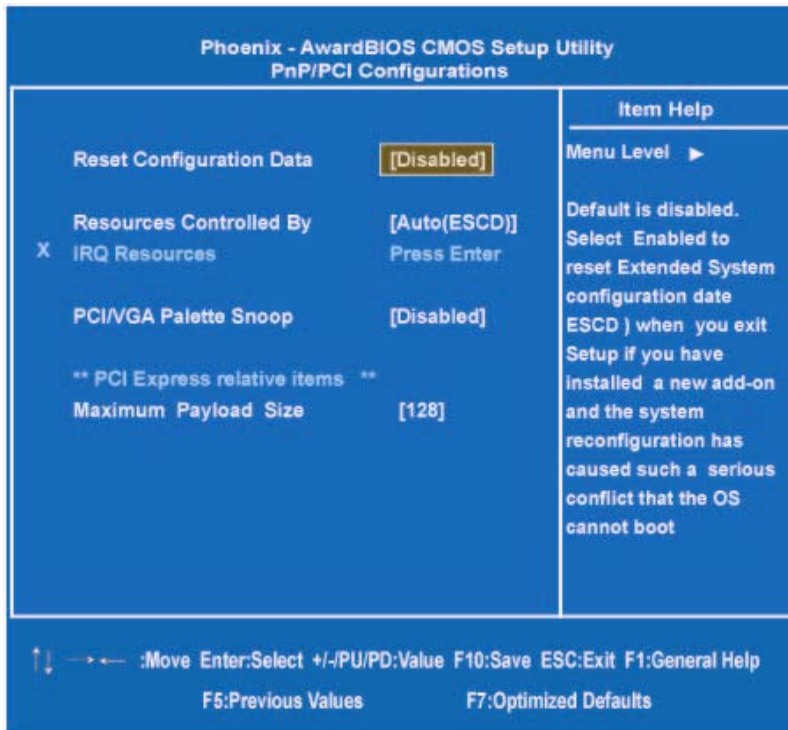
The function is always “*Enabled*”.

CPU THRM Throttling

It allows you to set CPU throttling percentage of 25%, 50%, and 75%.

4.10 PnP/PCI Configuration Setup

This section describes the configuration of PCI (Personal Computer Interconnect) bus system, which allows I/O devices to operate at speeds close to the CPU speed while communicating with other important components. This section covers very technical items that only experienced users could change default settings.



Reset Configuration Data Normally, you leave this item Disabled. Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup or if installing a new add-on cause the system reconfiguration a serious conflict that the operating system can not boot. Options: Enabled, Disabled.

Resources Controlled By The Award Plug and Play BIOS can automatically configure all boot and Plug and Play-compatible devices. If you select Auto, all interrupt request (IRQ), DMA assignment, and Used DMA fields disappear, as the BIOS automatically assigns them. The default value is "Manual".

IRQ Resources

When resources are controlled manually, assign each system interrupt to one of the following types in accordance with the type of devices using the interrupt:

1. Legacy ISA Devices compliant with the original PC AT bus specification, requiring a specific interrupt (such as IRQ4 for serial port 1).
- 2 . PCI/ISA PnP Devices compliant with the Plug and Play standard,

whether designed for PCI or ISA bus architecture. The default value is "PCI/ISA PnP".

PCI/VGA Palette Snoop

Some non-standard VGA display cards may not show colors properly. This item allows you to set whether MPEG ISA/VESA VGA Cards can work with PCI/VGA or not. When enabled, a PCI/VGA can work with a MPEG ISA/VESA VGA card; when disabled, a PCI/VGA cannot work with a MPEG ISA/VESA Card.

**** PCI Express relative items ****

Maximum Payload Size

When using DDR SDRAM and Buffer size selection, another consideration in designing a payload memory is the size of the buffer for data storage. Maximum Payload Size defines the maximum TLP (Transaction Layer Packet) data payload size for the device.

Press <Esc> to return to the Main Menu page.

4.11 PC Health Status

This section supports hardware monitoring that lets you monitor those parameters for critical voltages, temperatures and fan speed of the board.

Phoenix - AwardBIOS CMOS Setup Utility		Item Help
PC Health Status		Menu Level ▶
Current CPU Temperature	58°C/136°F	
Current System Temperature	56°C/132°F	
Vcore (V)	0.84 V	
VTT (V)	1.08V	
+ 3.3 (V)	3.40 V	
+ 5 (V)	5.09 V	
VBAT(V)	3.36 V	
5VSB(V)	5.16 V	

↑ ↓ → ← :Move Enter:Select +/-PU/PD:Value F10:Save ESC:Exit F1:General Help
 F5:Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults

System Component Characteristics

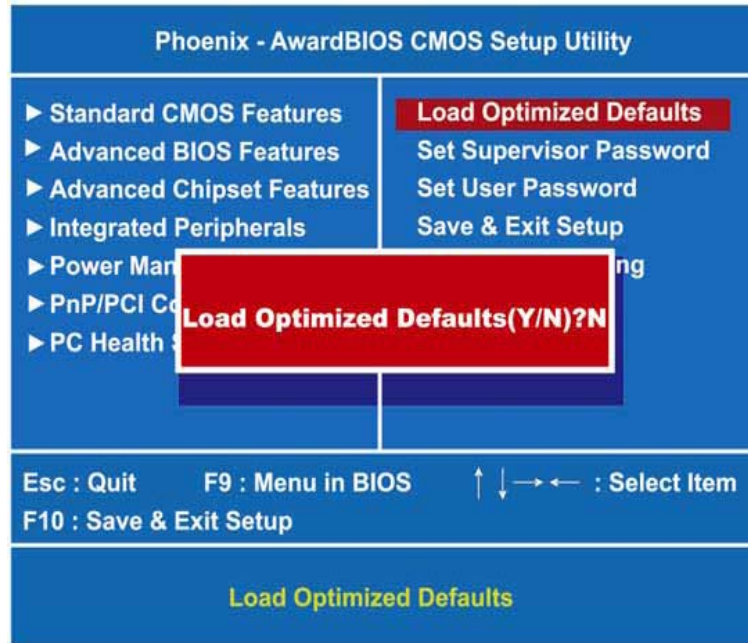
These items provide you with information about the system's current operating status. You can't change these items.

- 1 Current CPU Temperature
- 2 Current MCH Temperature
- 3 Current System Temperature
- 4 VCORE/VIT/VBAT/5VSB

Press <Esc> to return to the Main Menu page.

4.12 Load Optimized Defaults

This option allows you to load your system configuration with default values. These default settings are optimized to enable high performance features.



To load CMOS SRAM with SETUP default values, please enter “Y”. If not, please enter “N”.

4.13 Set Supervisor/User Password

You can set a supervisor or user password, or both of them. The differences between them are:

- 1 **Supervisor password:** You can enter and change the options on the setup menu.
- 2 **User password:** You can just enter, but have no right to change the options on the setup menu.

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

ENTER PASSWORD

Type a maximum eight-character password, and press <Enter>. This typed password will clear previously entered password from the CMOS memory. You will be asked to confirm this password. Type this password again and press <Enter>. You may also press <Esc> to abort this selection and not enter a password.

To disable the password, just press <Enter> when you are prompted to enter a password. A message will confirm the password is getting disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED

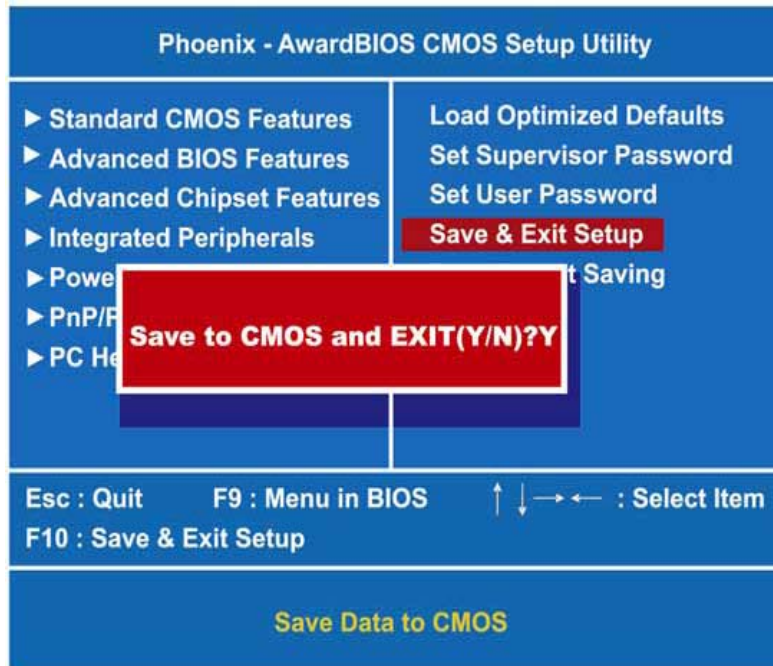
When a password is enabled, you have to type it every time you enter the Setup. It prevents any unauthorized persons from changing your system configuration.

Additionally, when a password is enabled, you can also require the BIOS to request a password every time the system reboots. This would prevent unauthorized use of your computer.

You decide when the password is required for the BIOS Features Setup Menu and its Security option. If the Security option is set to "System", the password is required during booting up and entry into the Setup; if it is set as "Setup", a prompt will only appear before entering the Setup.

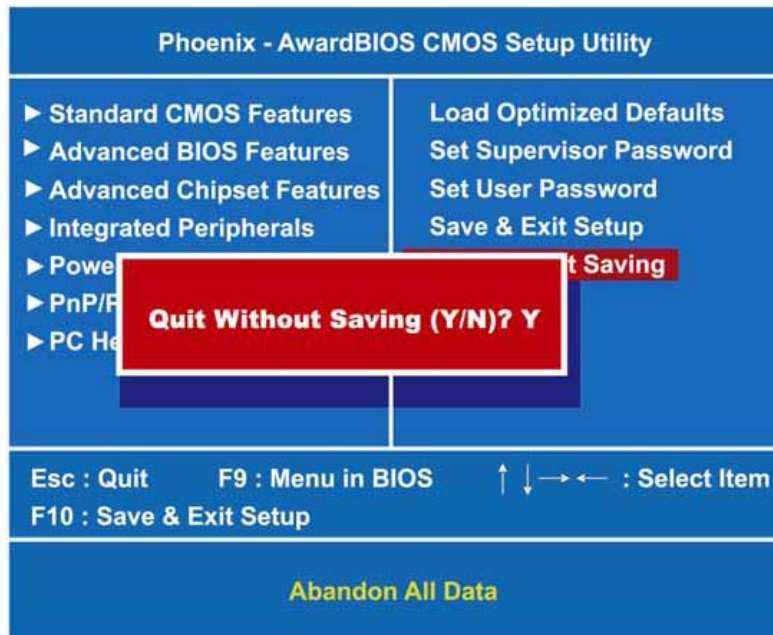
4.14 Save & Exit Setup

This section allows you to determine whether or not to accept your modifications. Type “Y” to quit the setup utility and save all changes into the CMOS memory. Type “N” to bring you back to the Setup utility.



4.15 Exit Without Saving

Select this option to exit the Setup utility without saving changes you have made in this session. Type “Y”, and it will quit the Setup utility without saving your modifications. Type “N” to return to the Setup utility.



CHAPTER 5

INSTALLATION OF DRIVERS

The device drivers are located on the Product Information CD-ROM that comes with the PICO821 Series package. The auto-run function of drivers will guide you to install the utilities and device drivers under a Windows system.

You can follow the onscreen instructions to install these devices:

- „ Chipset
- „ VGA
- „ LAN
- „ Audio

5.1 Installing Chipset Driver

- 1 Run the **SETUP.EXE** program from the driver directory in your
- 2 An Intel License Agreement appears to show you the important information. Click “Yes” to next step.
- 3 Please wait while running the following setup operations.





(3-1)



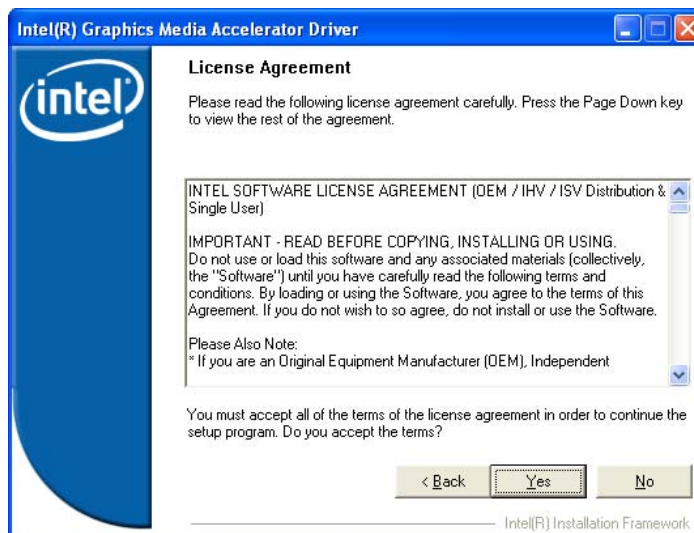
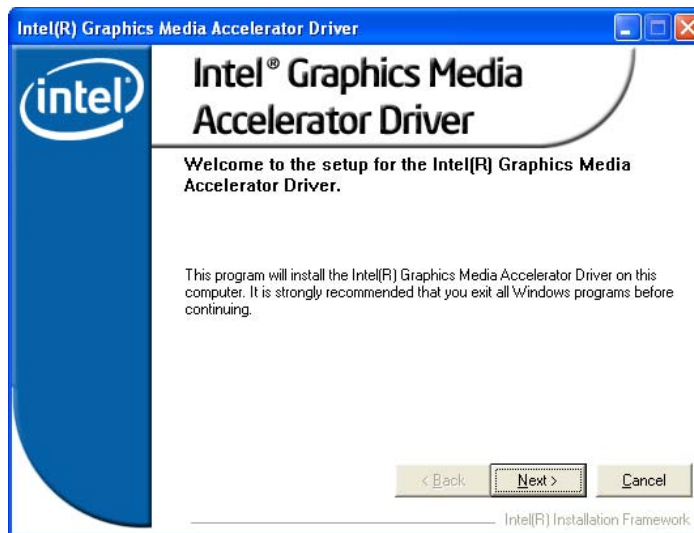
(3-2)

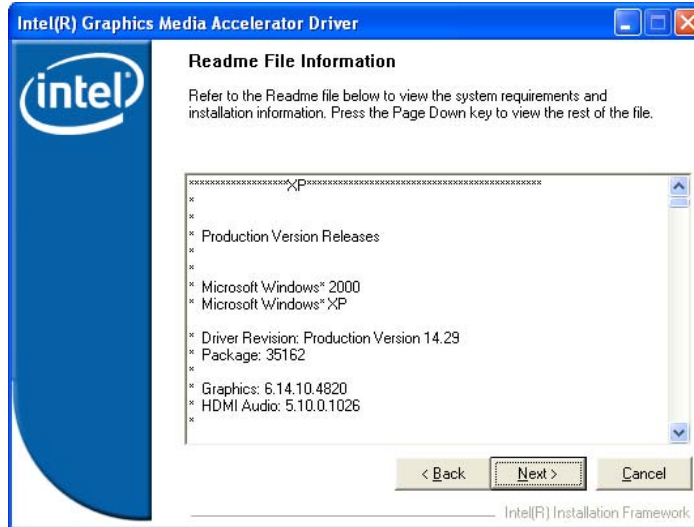
- 1 Click "Finish" to complete the setup process.
- 2 You will be asked to reboot your computer when the installation is completed. Please click "Yes, I want to restart my computer now" if you don't need to install any other drivers. Otherwise, please click "No, I will restart my computer later", and go on next step.



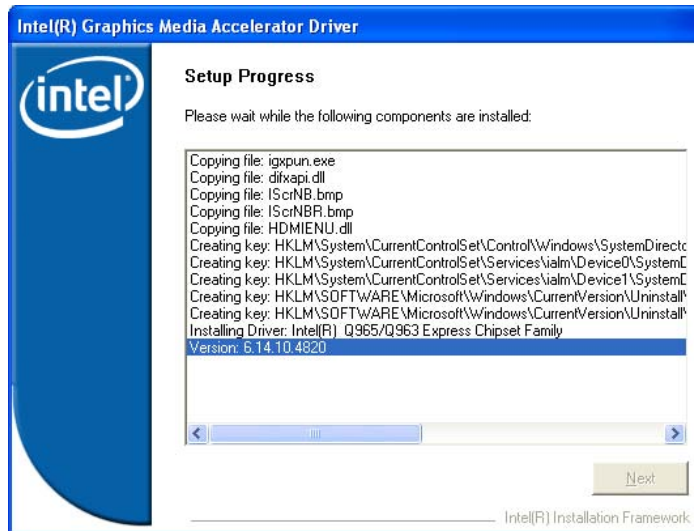
5.2 Installing VGA Driver

- 1 Run the SETUP.EXE program from the driver directory in your driver CD. Click “Next” to next step.
- 2 An Intel® License Agreement appears to show you the important information. Click “Yes” to next step.
- 3 The message of Readme File Information appears to show you the system requirements and installation information. Please click “Next”.

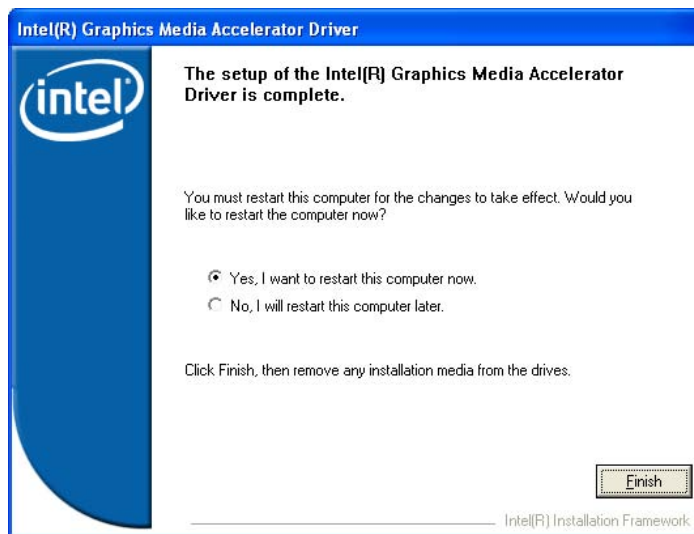
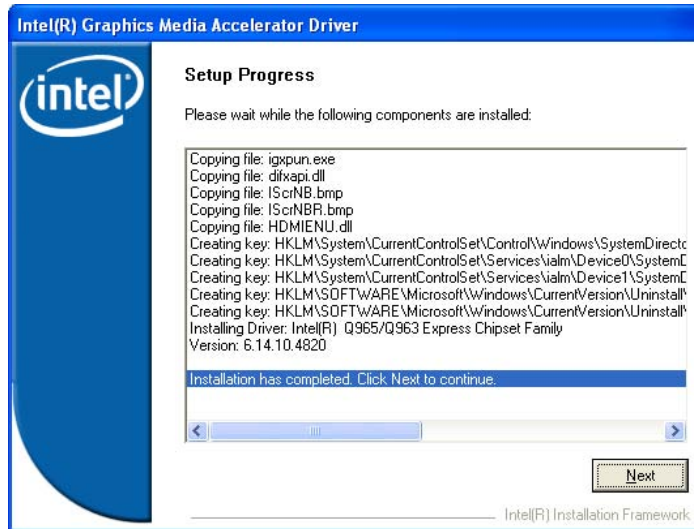




4. Please wait while running the following setup operations.



- 1 When this message appears, please click “Next”.
- 2 You will be asked to reboot your computer when the installation is completed. Please click “Yes, I want to restart my computer now” if you don’t need to install any other drivers. Otherwise, please click “No, I will restart my computer later”, and click “Finish” to complete the installation.



5.3 Installing LAN Driver

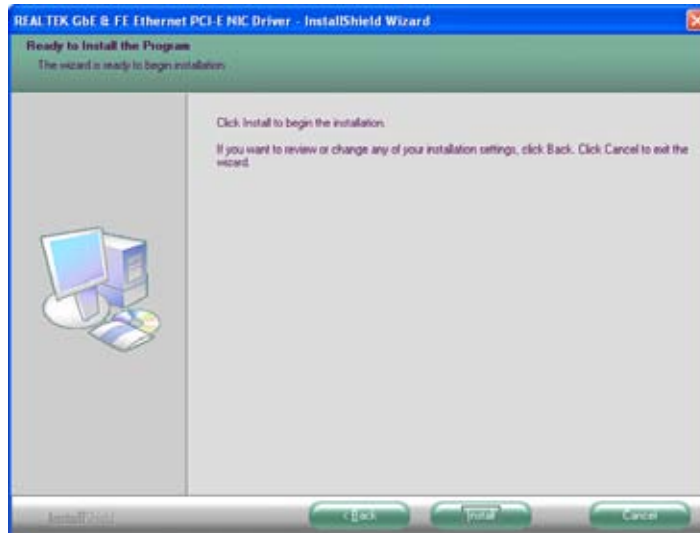
Run the InstallShield Wizard for Ethernet from the driver
Click "Install" to start the installation.

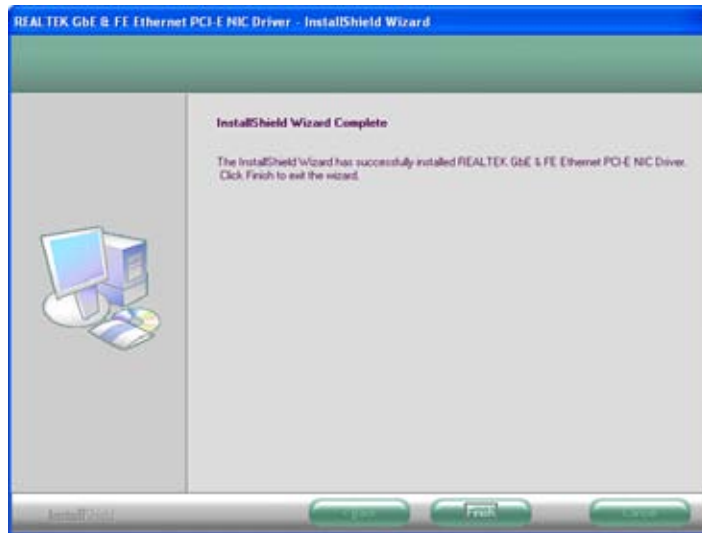
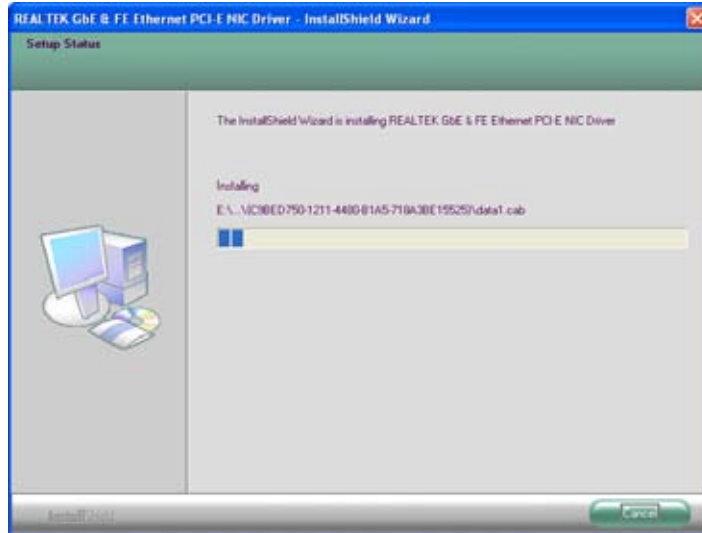
3. Please wait while running the following installation operation.
4. Click "Finish" to complete the installation.

5.4 Installing Audio Driver

Run the InstallShield Wizard for Audio from the driver directory in your driver CD. Click “Next” to next step.

Please wait while running the following installation operation.







3. You will be asked to reboot your computer when the installation is completed. Please click “Yes, I want to restart my computer now” if you don’t need to install any other drivers. Otherwise, please click “No, I will restart my computer later”, and click “Finish” to complete the installation.



APPENDIX A WATCHDOG TIMER

Watchdog Timer Setting (From Super I/O W83627HG-AW)

After the system stops working for a while, it can be auto-reset by the Watchdog Timer. The integrated Watchdog Timer can be set up in the system reset mode by program.

Using the Watchdog Function

Start

↓

Un-Lock WDT:

O 2E 87 ; Un-lock super I/O
O 2E 87 ; Un-lock super I/O

↓

Select Logic device:

O 2E 07
O 2F 08

↓

Activate WDT:

O 2E 30
O 2F 01

↓

Set Second or Minute :

O 2E F5
O 2F N N=00 or 08 (See below table)

↓

Set base timer :

O 2E F6
O 2F M=00,01,02,...FF(Hex) ,Value=0 to 255

↓

; IF to disable WDT:

O 2E 30
O 2F 00 ; Can be disable at any time

Timeout Value Range

- 1 to 255
- Minute / Second
- Program Sample

Watchdog Timer can be set to system reset after 5-second timeout.

2E, 87	
2E, 87	
2E, 07	
2F, 08	Logical Device 8
2E, 30	Activate
2F, 01	
2E, F5	
2F, N	Set Minute or Second N=08 (Min),00(Sec)
2E, F6	
2F, M	Set Value M = 00 ~ FF

APPENDIX B DIGITAL I/O

Digital I/O Software Programming

- GPI program sample:

O 2E 87	Un-Lock Super I/O
O 2E 87	Un-Lock Super I/O
O 2E 07	Select Logic device
O 2F 07	
O 2E F0	IO selection register
O 2F FF	Set GPI
O 2E F1	
I 2F	XX is input Data;if no input source,the value is 3F
Q	Quit debug

- GPO program sample:

O 2E 87	Un-Lock Super I/O
O 2E 87	Un-Lock Super I/O
O 2E 07	Select Logic device
O 2F 07	
O 2E F0	IO selection register
O 2E 00	Set GPO
O 2F F1	
O 2F XX	XX is output DATA
Q	Quit debug